PN-JUNCTION

- **1** The PN-junction in Equilibrium
- **2** The I–V Characteristics of the PN-Junction
- **3** Deviations from the Ideal Diode



 \ominus acceptor atom (negative ion) _ (free) electron



Notice that each piece of silicon is electrostatically **neutral** on the macroscopic level; there are equal numbers of positive and negative charges.

PN-junction



"Contactless" pn-junction





With no external voltage applied to the *p-n* junction, the diffusion and drift currents balance exactly, and there is no net current flow.

http://www.pveducation.org/pvcdrom/pn-junction/formation-pn-junction

pn-junction in thermal equilibrium



 $V_{AB} = 0$

* electrons (holes) diffuse from the n (p) side to the p (n) side, leaving behind N_D^+ (N_A^-) ionized donor (acceptor) atoms and, consequently, a net charge density $\rho \neq 0$, which gives rise to an electric field $\neq 0$

* pn = n_i^2 because $V_{AB} = 0$

pn-junction in thermal equilibrium



Introduction to Microelectronics

3. The Depletion Approximation

- Assume the QNR's are perfectly charge neutral
- Assume the SCR is <u>depleted</u> of carriers
 - depletion region
- Transition between SCR and QNR's sharp at



$$x < -x_{po}; \qquad P_{o}(x) = N_{A}, \qquad n_{o}(x) = \frac{n_{i}^{2}}{N_{A}}$$
$$-x_{po} < x < 0; \qquad p_{o}(x), \qquad n_{o}(x) \ll N_{A}$$
$$0 < x < x_{no}; \qquad n_{o}(x), \qquad p_{o}(x) \ll N_{D}$$
$$x > x_{no}; \qquad n_{o}(x) = N_{D}, \qquad P_{o}(x) = \frac{n_{i}^{2}}{N_{D}}$$

Space Charge Density



$\rho(\boldsymbol{x})=0;$	$x < -x_{po}$
$=-qN_A$;	$-x_{po} < x < 0$
$= qN_D$;	$0 < x < x_{no}$
= 0;	$x > x_{no}$

A review on Poisson's equation

 ρ - charge density ϵ - permittivity A - area





The Built-in Voltage



Depletion-layer width and maximum electric field







4. Contact Potential



Question 1: If I apply a voltmeter across the pn junction diode, do I measure ϕ_B ?

Question 2: If I short terminals of pn junction diode, does current flow on the outside circuit?

We are missing *contact potential* at the metalsemiconductor contacts:



Metal-semiconductor contacts: junction of dissimilar materials

 \Rightarrow built-in potentials at contacts φ_{mn} and $\varphi_{mp}.$

Potential difference across structure must be zero \Rightarrow Cannot measure ϕ_B .

$$\phi_{\boldsymbol{B}} = |\phi_{mn}| + |\phi_{mp}|$$



Biased pn-junction

Voltage drop at the ohmic contacts remain the same;

• Voltage drops across the quasi-neutral regions is zero (not valid for high currents);

 \rightarrow All applied voltage drops across the space charge region \rightarrow Electrostatics of the SCR under bias is unchanged from thermal equilibrium



Biased pn-junction

Electrostatics of the SCR under bias is unchanged from thermal equilibrium



PN-Junction in Thermal Equilibrium



Fig. 3.1 Thermal equilibrium: energy band diagram and carrier flux.

Thermal equilibrium: energy band diagram and carrier flux



 $J_n = J_n \text{ diff.} + J_n \text{ drift} \approx J_n \text{ drift}$ $J_p = J_p \text{ diff.} + J_p \text{ drift} \approx J_p \text{ drift}$

Introduction to Microelectronics

21

PN-Junction Under Forward Bias



$$J_{n} = J_{n} diff. + J_{n} drift \approx J_{n} diff.$$
$$J_{p} = J_{p} diff. + J_{p} drift \approx J_{p} diff.$$
$$J = J_{n} + J_{p}$$
diffusion current

 $V_A > 0$

The voltage drop across the (quasi)-neutral regions is ≈ zero for low-level injection



A few holes on the P-side approach the barrier with enough energy to carry over it and reach the N-side, where they recombine Boltzmann

I_{IP} is balanced by a continual generation of pairs by thermal fluctuations near the junction on the N-side and some of the holes produced fall down the energy gradient into the P-side giving a current I_{DO}

 I_{UP} – current uphill $\alpha N_A \exp(-\phi_I / \phi_f)$ I_{DO} – current downhill αp (N-side)

Equilibrium: $I_{IIP} = I_{DO} = I_S \alpha N_A \exp(-\phi_I / \phi_t)$

First order model: I_{DO} is independent of V_A – the rate of thermal generation of pairs will not change for $V_A \neq 0$ since it depends only on local properties of the crystal near the junction.

M. Born, Atomic Physics, Dover, p. 305 R. Feynman et al., The Feynman Lectures on Physics, Addison Wesley, vol. 3, p. 14.8.

Introduction to Microelectronics

VA



$$I_{D} = I_{UP} - I_{DO} = I_{S} [exp (V_{A} / \phi_{t}) - 1]$$

First order model is independent of V_A – the rate of thermal generation of pairs will not change for $V_A \neq 0$ since it depends only on local properties of the crystal near the junction.

M. Born, Atomic Physics, Dover, p. 305 R. Feynman et al., The Feynman Lectures on Physics, Addison Wesley, vol. 3, p. 14.8.

Development of analytical dc model (I-V characteristics) of the diode





At the edges of the depletion region, $-x_p$ and x_n , equilibrium conditions do not prevail so we must use the "**law of the junction**".



$$\frac{p(-x_p)}{p(x_n)} = \exp\left(\frac{q\left[\phi(-x_p) - \phi(x_n)\right]}{kT}\right) = \exp\left[\frac{-(V_A - \phi_J)}{\phi_t}\right]$$





Boundary conditions for minority carriers



Calculate the current-voltage characteristic of a "short" P+N junction diode

- 1. Holes are the main carriers;
- 2. Recombination is negligible in the N region;
- 3. Diffusion current is dominant







Diode behavior near the origin with $I_s=10^{-15}$ A and n=1



References

- EEL 7061 Eletrônica Básica
 http://www.lci.ufsc.br/electronics/index7061.htm
- Reid R. Harrison, "Analog Integrated Circuit Design" ECE/CS 5720/6720 Department of Electrical and Computer Engineering University of Utah
- Charles Sodini, "6.012 Microelectronic Devices and Circuits", OpenCourseWarehttp://ocw.mit.edu
- Sze & Ng, "Physics of semiconductor devices", 3rd edn. Wiley
- Pierret, "Semiconductor device fundamentals," Addison-Wesley